ABSTRACT

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2 A baseband receiver having quadrature analog outputs 3 and a plurality of analog control and status signals and a 4 transmit modulator having analog quadrature inputs and a 5 plurality of analog control and status signals are coupled 6 to a transmit processor having a digital output and a plurality of digital control and status signals and to a 7 8 receive processor having a digital input and a plurality of 9 digital control and status signals by multiplexing analog to 10 digital converters and digital to analog converters such 11 that during a receive time the converters are used for a receive purpose and during a transmit time, the converters 12 13 are used for a transmit purpose. 14